



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

In re Application of:

Stephen M. Watts

Docket: TI-29923

Serial No.: 10/026,085

Art Unit: 8279

Filed: 12/21/2001

Examiner: P. Patel

For: PARALLEL INTEGRATED CIRCUIT TEST APPARATUS AND  
TEST METHOD

MS PGPUB Drawings  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING 37 CFR §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below.

3-2-04

Jackie McBride  
Jackie McBride

LETTER TO THE OFFICIAL DRAFTSPERSON

Sir:

Please find enclosed five (5) sheets of formal drawings with the changes as required by the Examiner in the Notice of Allowance dated December 3, 2003.

Texas Instruments Incorporated  
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Respectfully submitted,

Yingsheng Tung  
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